PTO/SB/08A (10-01)
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(use as many sheets as necessary)*

Sheet 1 of 2

| *Complete if Known* | |
|---|---|
| Application Number | 09/880,749 |
| Filing Date | June 12, 2001 |
| First Named Inventor | Draper, Andrew M. |
| Art Unit | To Be Assigned |
| Examiner Name | To Be Assigned |
| Attorney Docket Number | 015114-053600US |

## U.S. PATENT DOCUMENTS

| Examiner | Cite No.[1] | Document Number Number Kind Code[2] (*if known*) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
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| | 0004 | US-5,258,668 | 11-02-1993 | Cliff et al. | |
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| Examiner Initials* | Cite No.[1] | Foreign Patent Document Country Code[3] Number[4] Kind Code[5] (*if known*) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | T[6] |
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| Examiner Signature | | Date Considered | 2/25/2005 |
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

| Complete if Known | |
|---|---|
| Application Number | 09/880,749 |
| Filing Date | June 12, 2001 |
| First Named Inventor | Draper, Andrew M. |
| Art Unit | To Be Assigned |
| Examiner Name | To Be Assigned |
| Attorney Docket Number | 015114-053600US |



## OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials * | Cite No.[1] | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T[2] |
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| | 0011 | "AT94K Series Field Programmable System Level Integrated Circuit," Advance Information Brochure of Atmel Corporation, December 1999, 6 pages. | |
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| Examiner Signature | | Date Considered | 2/25/2004 |
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OIPE JC176 JUL 27 2001 PATENT & TRADEMARK OFFICE

| FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | Attorney Docket No.: 015114-053600US | Application No.: 09/880,749 |
|---|---|---|
| | Applicant: Andrew Draper | |
| | Filing Date: June 12, 2001 | Group: ~~Unassigned~~ 2133 |

Reference Designation — **U.S. PATENT DOCUMENTS** — Page 1

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| ___ AA | 4,488,259 | Dec. 11, 1984 | Mercy | 364 | 900 | |
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| | Document No. | Date | Country | Class | Sub-class | Translation (Yes/No) |
|---|---|---|---|---|---|---|
| ___ AF | | | | | | |
| ___ AG | | | | | | |
| ___ AH | | | | | | |
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**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

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| ___ AI | Altera "APEX 20K Programmable Logic Device Family Data Sheet", May 1999 |
| ___ AJ | Altera "FLEX 10K Embedded Programmable Logic Family Data Sheet", May 1999 |
| ___ AK | Altera "FLEX 8000 Programmable Logic Device Family Data Sheet", May 1999 |
| ___ AL | Altera "MAX 7000 Programmable Logic Device Family Data Sheet", May 1999 |
| ___ AM | Altera "IEEE 1149.1 (JTAG) Boundary-Scan Testing in Altera Devices" August 1999, Application Note 39 |
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